

11002 U.S. PTO  
10/080683



02/25/02

76943

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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL. NUM 10080683	FILING DATE 02/25/2002	CLASS 257	SUBCLASS 797	GAU 2811	EXAMINER Owens
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\*\*APPLICANTS: Tomita Kazuo; Ueno Atsushi;

\*\*CONTINUING DATA VERIFIED:

THIS APPLICATION IS A CON OF 09/484,066 01/18/2000 PAT 6,376,924

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\*\* FOREIGN APPLICATIONS VERIFIED:

JAPAN P11-206305 07/21/1999

PG-PUB DO NOT PUBLISH ☐ RESOUND ☐

Foreign priority claimed ☐ yes ☐ no

35 USC 113 conditions met ☐ yes ☐ no

Verified and acknowledged Examiners's initials

FILE: Semi-conductor device with an integrated circuit and a control circuit for the same

ATTORNEY DOCKET NO.  
1930 US2 CONT

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
Assistant Examiner			
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> <b>TERMINAL DISCLAMER</b>		Primary Examiner Application Examiner	
		<b>PREPARED FOR ISSUE</b>	
<b>WARNING:</b> The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

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